

NEWS RELEASE

Company Contacts:

Trudy Weiser
Wavecrest Marketing Communications Manager
(952) 646-0477
tweiser@wavecrest.com

WAVECREST ANNOUNCES NEW SOLUTIONS MEETING PCI EXPRESS™ 1.1 SPECIFICATION

Provided Committee Co-chairmanship and Written Contribution to New Spec

International Test Conference, Charlotte, NC – Oct. 26, 2004 – Wavecrest, the leading developer of signal integrity analysis solutions for engineers in design, characterization and production environments announced that their new SIA Family of Signal Integrity Analysis Solutions meet the PCI Express 1.1 Specification currently in the final stages of approval by PCI-SIG. Wavecrest co-chaired the PCI-SIG Jitter Working Group and made major contributions to the new spec. These new solutions will be demonstrated at booth 1221 during ITC in Charlotte, NC on October 26-28, 2004.

The Wavecrest Signal Integrity Analysis solution for PCI Express consists of the company's enhanced SIA 3300D or SIA 3600D Signal Integrity Analyzer and easy-to-use GigaView™ software. These solutions include a dedicated PCI Express software tool that tests all physical layer jitter parameters with immediate pass/fail confirmation on one screen. Amplitude parameters such as rise-time, fall-time, and eye opening can also be tested. The GigaView software provides additional diagnostic tools that enable the user to instantly determine the root cause of signal integrity issues.

GigaView software utilizes Wavecrest's patented TailFit™ algorithm, an industry standard approved methodology for separating and identifying various jitter components and providing a true and accurate total jitter number. This unique capability allows the user to predict the future performance of a device or system to a BER of 10^{-12} or lower, a requirement of the 1.1 Specification.

The new Wavecrest SIA solutions also include a built-in programmable clock recovery that provides 1st order high-pass function for testing jitter. The PCI Express 1.1 Specification updated the requirement in the 1.0a Specification from 3rd order high-pass function to 1st order, to ensure more accuracy during jitter testing.

"The 2.5 Gb/s speeds of PCI Express applications require even more critical testing methodologies so that overall device performance is ensured," states Dennis Leisz, Wavecrest President and CEO. "The new PCI Express 1.1 Specification has been written in part to ensure more accuracy in jitter testing of these high volume applications. Now with Wavecrest's new solutions, engineers can be confident that their test results will meet the new specification, and their devices will be reliable and high-performing when released to market and will have a higher yield during production."

Wavecrest is taking orders immediately for SIA Signal Integrity Analysis solutions meeting the PCI Express 1.1 Specification for shipment in January '05.

About Wavecrest

Within design, characterization and production facilities worldwide, engineers trust Wavecrest solutions to meet their toughest challenges in signal integrity analysis. Wavecrest signal integrity solutions are optimized for clock and datacom applications in both lab and ATE environments. For more information, please visit www.wavecrest.com.

###

PCI Express is a trademark of PCI-SIG.

GigaView and Tailfit are trademarks of Wavecrest Corp.

Other trademarks that may be mentioned in this release are the intellectual property of their respective owners.