

## **NEWS RELEASE**

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## **Wavecrest Announces Successful Demonstration of the SIA-3000 on Advantest's T2000 Semiconductor Test System**

*Signal Integrity Tools enable compliance testing on OPENSTAR™*

**Eden Prairie, MN – July 14, 2004** – Wavecrest, the reference standard for accurate, repeatable and compliant signal integrity measurement instruments, today announced successful demonstration of the capabilities of its SIA-3000 Signal Integrity Analysis Solution on the T2000 Series test system from Advantest Corp. (TSE: 6857, NYSE: ATE). The T2000 is the first system to meet the Open Semiconductor Test Architecture (OPENSTAR™) standard, developed and implemented by the Semiconductor Test Consortium (STC).

STC Secretary and Administrator Fred Bode stated, "We are very pleased with the demonstration results of the SIA-3000. The availability of this module for our first OPENSTAR system implementation is important to customers who are looking for reliable signal integrity analysis, and we look forward to working closely with Wavecrest to ensure customer requirements are not only met but exceeded."

Like Advantest, Wavecrest is a charter member of the STC and has been contributing to the STC working groups since their inception in June 2002. The SIA-3000 demonstration on the T2000 system is the starting point for many new products slated for the T2000 over the next few years.

Wavecrest demonstrated the testing of a PCI Express™ device on the T2000 in front of customers during an event held Tuesday, June 15, 2004. The company also demonstrated both the SIA-3000 suite of characterization/engineering tools, called Gigaview™, and production testing of the PCI Express device with its production test API libraries.

"We are very excited to be working with STC to provide comprehensive integration of our signal integrity analysis solutions for customers of OPENSTAR-compliant testers," stated Dennis Leisz, president and CEO of Wavecrest. "Compliance testing is a crucial step for silicon developers. By providing tools that ensure accurate and repeatable signal integrity measurements during compliance testing, developers will have the confidence that their products will be brought to market faster."

The SIA-3000 enables thorough testing to prevailing compliance specifications for Reference Clocks & PLL's up to 6GHz, Rambus® devices, Serial ATA and SAS, PCI Express @ 2.5Gbps, Fibre Channel 1x, 2x and 4x speeds, Gigabit Ethernet, XAUI @ 3.125 Gbps, Rapid I/O and HyperTransport™.

During the debug and characterization phase, it can be nearly impossible to accurately test the signal integrity of devices by conventional ATE systems because these devices run asynchronously. The SIA-3000's Gigaview™ software engineering tools put the power of a “Communications Signal Analyzer” into the T2000 system. These tools deliver true compliance measurements *and* diagnostic information to better characterize and predict the long-term performance of a device.

The SIA-3000 with Gigaview software accurately and repeatably quantifies random jitter (RJ), deterministic jitter (DJ), data-dependent jitter (DDJ), periodic jitter (PJ) and total jitter (TJ) to  $10^{-12}$  bit error rate (BER) or lower. The system will measure phase jitter of clocks and the skew between clock pins and data I/O bus pins. The SIA-3000 also enables quick and accurate measurement of device rise and fall times as fast as 60ps. After characterization of devices, facilities are able to go right into production with Wavecrest's library of high throughput tests designed to give true compliance results at production speeds.

System upgrade kit configurations vary depending on specific applications. Configurations start at 2 channels, 2.5Gbps data speeds and 6GHz clock & bandwidths and go to 8 channels of 4.5Gbps speeds. Automatic pattern marker generation and a programmable continuous rate recovered clock are optional. Gigaview engineering tools and Wavecrest production API libraries are standard with all systems. Deliveries on the T2000 OPENSTAR systems start in September of 2004.

### **About Wavecrest**

Within design, characterization and production facilities worldwide, engineers trust in Wavecrest solutions to meet their toughest challenges in signal integrity analysis. Wavecrest signal integrity solutions are optimized for clock and datacom applications in both lab and ATE environments. For more information, please visit [www.wavecrest.com](http://www.wavecrest.com).

### **About the Semiconductor Test Consortium**

The Semiconductor Test Consortium was founded in 2003 to develop a common test architecture that is completely open, documented and supported via solutions available from all ATE vendors. Open to all companies throughout the semiconductor supply chain with a vested interest in the test sector, the consortium is focused on the following goals: driving the direction of OPENSTAR; publishing the architecture and providing training programs and workshops to ensure it is truly open; and defining and managing validation procedures to ensure full vendor interoperability. More than 26 semiconductor, equipment and instrumentation companies worldwide and 11 universities in Europe, Japan, China and the United States now support the STC. More information on members, benefits and membership materials can be found at [www.semitest.org](http://www.semitest.org).